

PRODUCT SPECIFICATION

Part Number
PLH3535-WCUV02

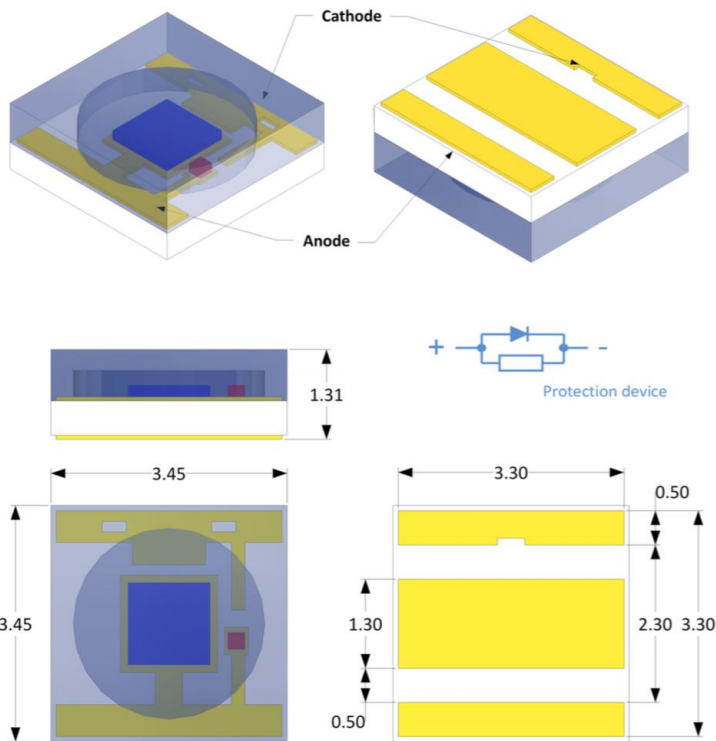
Details

- 3535 Ultraviolet Surface Mount LED
- 3.45 x 3.45 x 1.3 mm
- Aluminum Nitride substrate
- Packaged on 1,000 piece reel

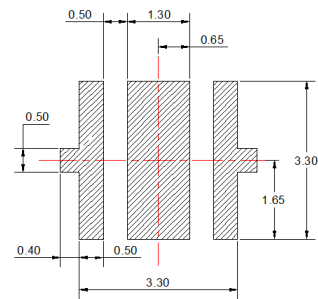
Features

- Durable and Rugged
- RoHS Compliant
- High Power

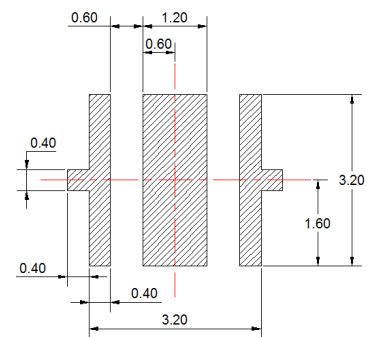
Outline Dimensions



Recommend Soldering Pad Design



Recommend Stencil Pattern Design (Marked Area is Opening)



Notes:

1. Dimensions in millimeters unless otherwise noted
2. Tolerance is ± 0.13 mm unless otherwise noted.
3. Specifications subject to change without notice





Device Selection Guide

Model Number	Chip		Resin
	Material	Emitting Color	
PLH3535-WCUV02	InGaAlN	Ultraviolet (UV)	Clear

Radiometric Power and Forward Voltage

Part Number	Color	Performance at Test Current 500mA					Calculated Performance at 700mA
		Group	Radiometric Power (mW)		V _f		Min. Radiometric Power (mW)
			Min.	Max.	Min.	Max.	
PLH3535-WCUV02	U2B (390~400nm)	NC5	180	200	3.4	4.4	250
		ND1	200	240	3.4	4.4	280
		ND2	240	280	3.4	4.4	335
		ND3	280	320	3.4	4.4	390
	U3A (400~410nm)	NC5	180	200	3.4	4.4	250
		ND1	200	240	3.4	4.4	280
		ND2	240	280	3.4	4.4	335
		ND3	280	320	3.4	4.4	390

Notes: 1. Radiometric power is measured with an accuracy of $\pm 10\%$.
 2. The forward voltage is measured with an accuracy of $\pm 0.2V$.



Absolute Maximum Ratings at Ta=25°C

Parameter	Rating
DC Forward Current (mA)	700mA
LED Junction Temperature	150°C
LED Operating Temperature	-20°C ~ 80°C
Storage Temperature	-40°C ~ 100°C
Soldering Temperature	Max. 260°C / Max. 10 sec. (JEDEC 020c)
ESD Sensitivity	2000 V HBM (JESD-22A-114-B)

Notes:

1. This device isn't designed to be driven in reverse bias.
2. Please confirm the junction temperature is under maximum rating.

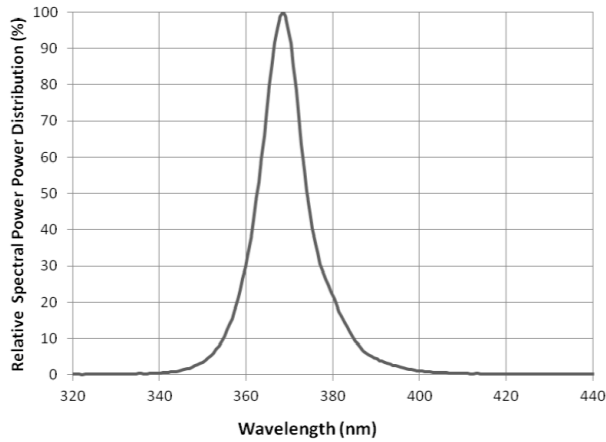
General Characteristics at 500mA

Part Number	Color	Peak Wavelength λ_p		$2\theta_{1/2}$	Temperature Coefficient of Vf (mV/°C)	Thermal Resistance Junction to Pad (°C/W)
		Min.	Max		$\Delta V_f / \Delta T_J$	$R_{\theta_{J-L}}$
PLH3535-WCUV02	U2B	365	370	110	-2~-4	4.4
	U3A	370	375	110	-2~-4	4.4

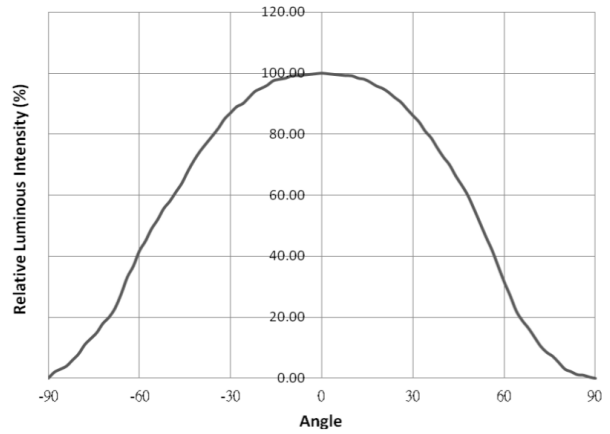
Notes:1. The peak wavelength is measured with an accuracy of ± 1 nm.

Electrical and Optical Curves

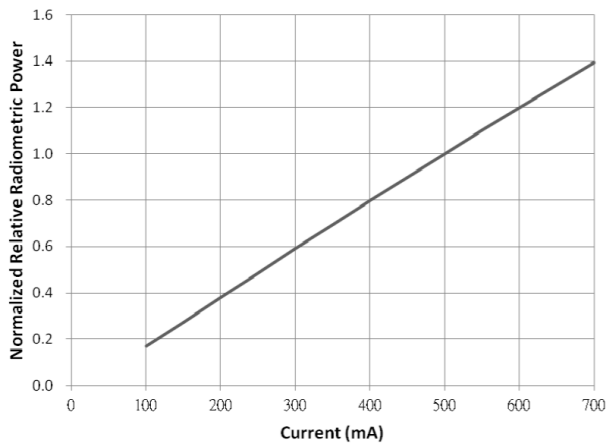
Relative Spectral Power Distribution, $T_j=25^\circ\text{C}$



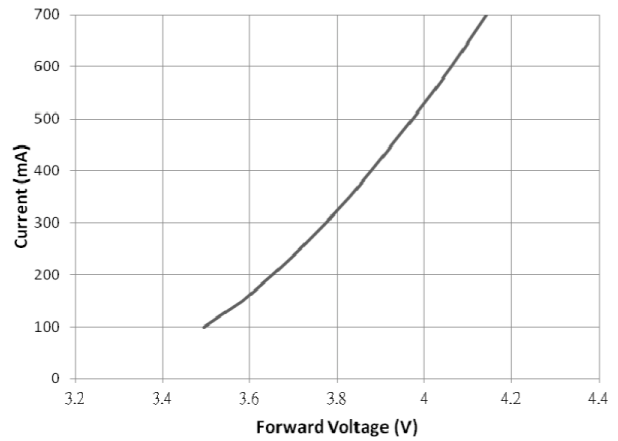
Typical Spatial Radiation Pattern



Typical Forward L-I Characteristics, $T_j=25^\circ\text{C}$

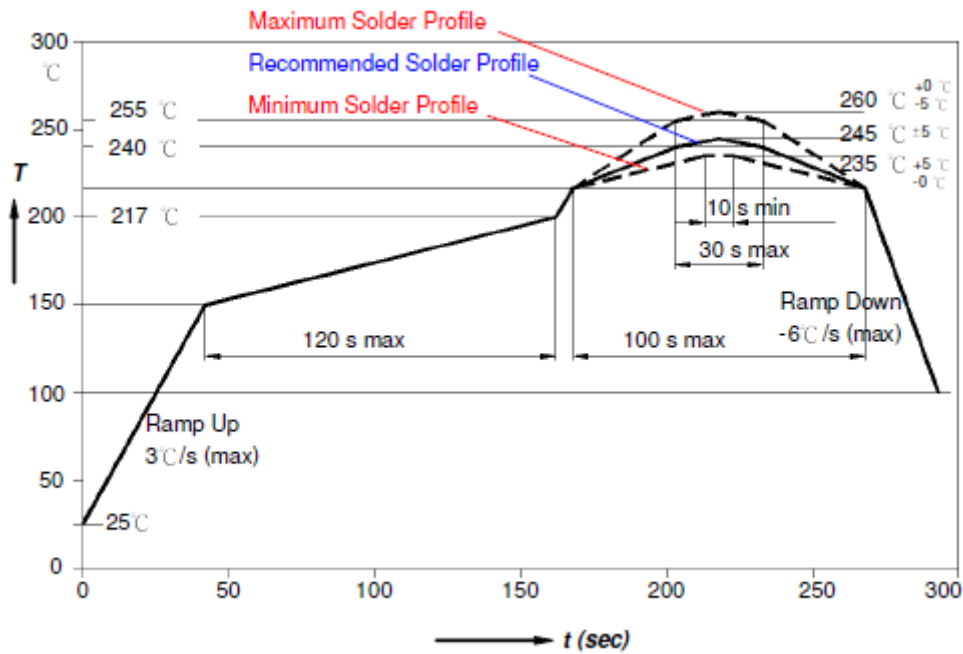


Typical Forward I-V Characteristics, $T_j=25^\circ\text{C}$



Reflow Soldering

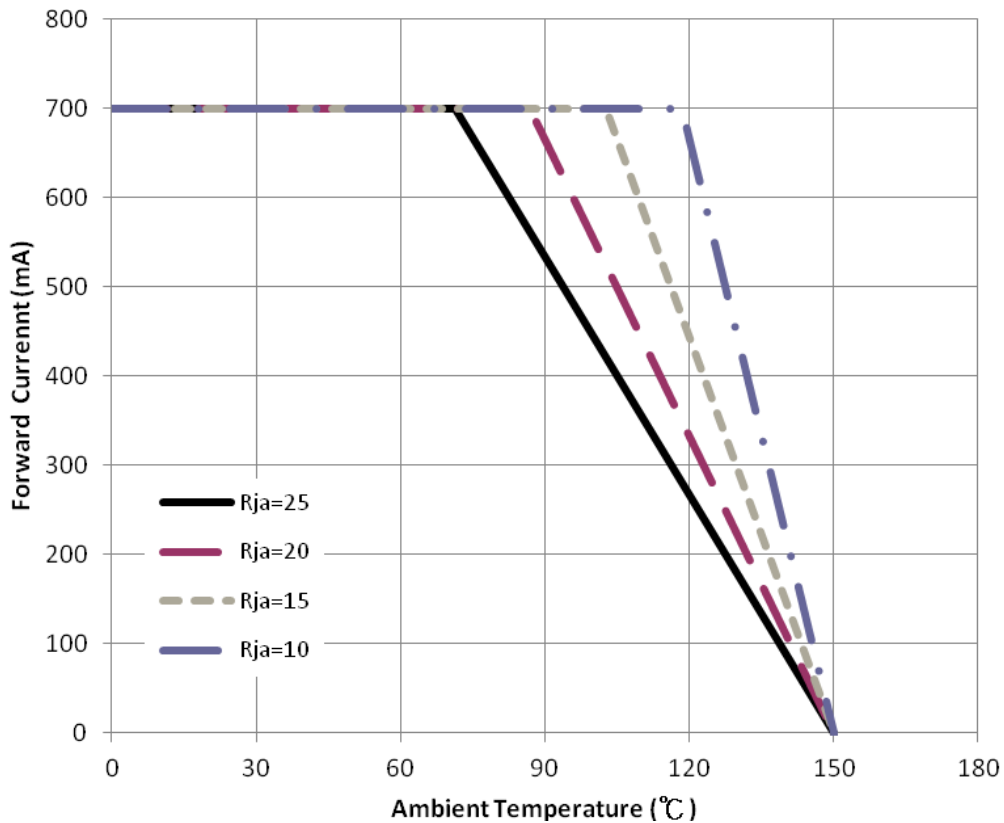
The LEDs can be soldered using the parameter listed below. As a general guideline, the users are suggested to follow the recommended soldering profile provided by the manufacturer of the solder paste. Although the recommended soldering conditions are specified in the list, reflow soldering at the lowest possible temperature is preferred for the LEDs.



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average Ramp-up Rate (T _{max} to T _p)	3°C/second max.	3°C/second max.
Preheat		
- Temperature Min(T _{min})	100°C	150°C
- Temperature Max(T _{max})	150°C	200°C
- Time(t _{min} to t _{max})	60-120 seconds	60-180 seconds
Time maintained above:		
- Temperature(T _L)	183°C	217°C
- Time(t _L)	60-150 seconds	60-150 seconds
Peak/classification Temperature(T _p)	215°C	260°C
Time within 5°C of actual Peak Temperature(t _p)	10-30 seconds	20-40 seconds
Ramp-Down Rate	6°C/second max.	6°C/second max.
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.

Thermal Design

Thermal design of the end product is important. The thermal resistance between the junction and the solder point (R_{θJ-P}) and the end product should be designed to minimize the thermal resistance from the solder point to ambient in order to optimize the emitter life and optical characteristics. The plot of Allowable Forward Current vs. Ambient Temperature determines the maximum operation current.

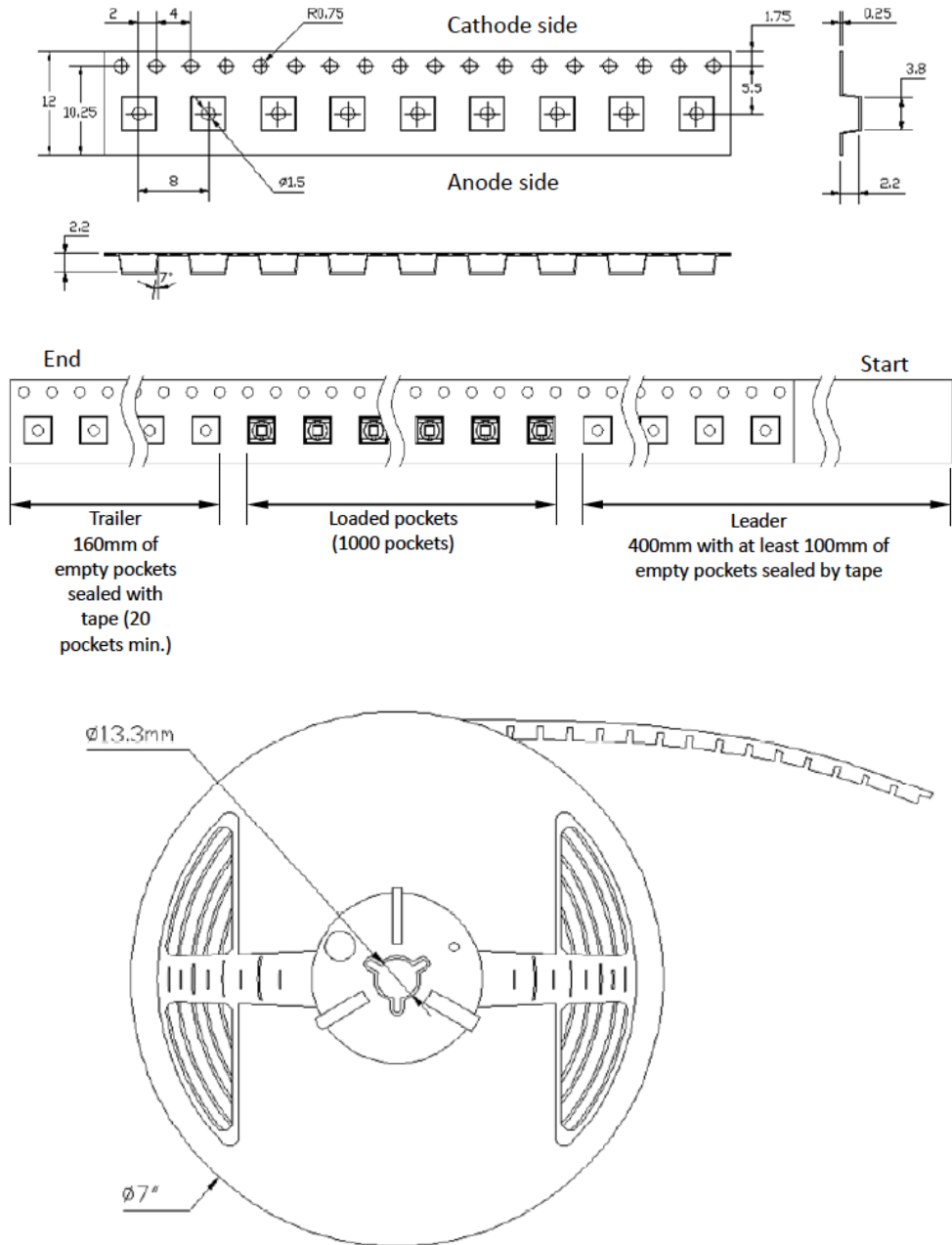


The junction temperature can be correlated to the thermal resistance between the junction and ambient (R_{ja}) by the following equation.

$T_j = T_a + R_{ja} * W$ T_j: LED junction temperature T_a: Ambient temperature R_{ja}: Thermal resistance between the junction and ambient W: Input power (I_F*V_F)

Packing Information

The carrier tape is conformal to EIA-481D



Note: All Dimensions are in millimeters



PLH3535-WCUV02 Customer Approval Signatures	Approved By	Checked By	Prepared By

<i>Record Of Revisions</i>			
Rev.	Description	Date	Page
0	Released Spec	08/28/15	--